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Geert Hellings
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High Mobility and Quantum Well Transistors

Design and TCAD Simulation

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High Mobility And Quantum Well Transistors Design And Tcad Simulation Springer Series In Advanced Microelectronics:

High Mobility and Quantum Well Transistors Geert Hellings,Kristin De Meyer,2013-03-25 For many decades the semiconductor industry has miniaturized transistors delivering increased computing power to consumers at decreased cost However mere transistor downsizing does no longer provide the same improvements One interesting option to further improve transistor characteristics is to use high mobility materials such as germanium and III V materials However transistors have to be redesigned in order to fully benefit from these alternative materials High Mobility and Quantum Well Transistors Design and TCAD Simulation investigates planar bulk Germanium pFET technology in chapters 2 4 focusing on both the fabrication of such a technology and on the process and electrical TCAD simulation Furthermore this book shows that Quantum Well based transistors can leverage the benefits of these alternative materials since they confine the charge carriers to the high mobility material using a heterostructure The design and fabrication of one particular transistor structure the SiGe Implant Free Quantum Well pFET is discussed Electrical testing shows remarkable short channel performance and prototypes are found to be competitive with a state of the art planar strained silicon technology High mobility channels providing high drive current and heterostructure confinement providing good short channel control make a promising combination for future technology nodes **Приборно-технологическое моделирование устройств микро- и наноэлектроники. Математические модели и программные средства** Павел Маринушкин,Сергей Трегубов,Алексей Левицкий,2022-05-15 110404 110000 *Advanced Electronic Technologies and Systems Based on Low-Dimensional Quantum Devices* M. Balkanski,Nikolai Andreev,2010-12-15 This volume on Advanced Electronic Technologies and Systems based on Low Dimensional Quantum Devices closes a three years series of NATO AS s The first year was focused on the fundamental properties and applications The second year was devoted to Devices Based on Low Dimensional Semiconductor Structures The third year is covering Systems Based on Low Dimensional Quantum Semiconductor Devices The three volumes containing the lectures given at the three successive NATO ASI s constitute a complete review on the latest advances in semiconductor Science and Technology from the methods of fabrication of the quantum structures through the fundamental physics am basic knowledge of properties and projection of performances to the technology of devices and systems In the first volume Fabrication Properties and Application of Low Dimensional Semiconductors are described the practical ways in which quantum structures are produced the present status of the technology difficulties encountered and advances to be expected The basic theory of Quantum Wells Double Quantum Wells and Superlattices is introduced and the fundamental aspects of their optical properties are presented The effect of reduction of dimensionality on lattice dynamics of quantum structures is also discussed In the second volume Devices Based on Low Dimensional Structures the fundamentals of quantum structures and devices in the two major fields Electro Optical Devices

and Pseudomorphic High Eectron Mobility Transistors are extensively discussed **Handbook for III-V High Electron Mobility Transistor Technologies** D. Nirmal,J. Ajayan,2019-05-14 This book focusses on III V high electron mobility transistors HEMTs including basic physics material used fabrications details modeling simulation and other important aspects It initiates by describing principle of operation material systems and material technologies followed by description of the structure I V characteristics modeling of DC and RF parameters of AlGaN GaN HEMTs The book also provides information about source drain engineering gate engineering and channel engineering techniques used to improve the DC RF and breakdown performance of HEMTs Finally the book also highlights the importance of metal oxide semiconductor high electron mobility transistors MOS HEMT Key Features Combines III As P N HEMTs with reliability and current status in single volume Includes AC DC modelling and sub millimeter wave devices with reliability analysis Covers all theoretical and experimental aspects of HEMTs Discusses AlGaN GaN transistors Presents DC RF and breakdown characteristics of HEMTs on various material systems using graphs and plots Junctionless Field-Effect Transistors Shubham Sahay,Mamidala Jagadesh Kumar,2019-01-25 A comprehensive one volume reference on current JLFET methods techniques and research Advancements in transistor technology have driven the modern smart device revolution many cell phones watches home appliances and numerous other devices of everyday usage now surpass the performance of the room filling supercomputers of the past Electronic devices are continuing to become more mobile powerful and versatile in this era of internet of things IoT due in large part to the scaling of metal oxide semiconductor field effect transistors MOSFETs Incessant scaling of the conventional MOSFETs to cater to consumer needs without incurring performance degradation requires costly and complex fabrication process owing to the presence of metallurgical junctions Unlike conventional MOSFETs junctionless field effect transistors JLFETs contain no metallurgical junctions so they are simpler to process and less costly to manufacture JLFETs utilize a gated semiconductor film to control its resistance and the current flowing through it Junctionless Field Effect Transistors Design Modeling and Simulation is an inclusive one stop referenceon the study and research on JLFETs This timely book covers the fundamental physics underlying JLFET operation emerging architectures modeling and simulation methods comparative analyses of JLFET performance metrics and several other interesting facts related to JLFETs A calibrated simulation framework including guidance on SentaurusTCAD software enables researchers to investigate JLFETs develop new architectures and improve performance This valuable resource Addresses the design and architecture challenges faced by JLFET as a replacement for MOSFET Examines various approaches for analytical and compact modeling of JLFETs in circuit design and simulation Explains how to use Technology Computer Aided Design software TCAD to produce numerical simulations of JLFETs Suggests research directions and potential applications of JLFETs Junctionless Field Effect Transistors Design Modeling and Simulation is an essential resource for CMOS device design researchers and advanced students in the field of physics and semiconductor devices *Fundamentals of Bias Temperature Instability in*

MOS Transistors Souvik Mahapatra,2015-08-05 This book aims to cover different aspects of Bias Temperature Instability BTI BTI remains as an important reliability concern for CMOS transistors and circuits Development of BTI resilient technology relies on utilizing artefact free stress and measurement methods and suitable physics based models for accurate determination of degradation at end of life and understanding the gate insulator process impact on BTI This book discusses different ultra fast characterization techniques for recovery artefact free BTI measurements It also covers different direct measurements techniques to access pre existing and newly generated gate insulator traps responsible for BTI The book provides a consistent physical framework for NBTI and PBTI respectively for p and n channel MOSFETs consisting of trap generation and trapping A physics based compact model is presented to estimate measured BTI degradation in planar Si MOSFETs having differently processed SiON and HKMG gate insulators in planar SiGe MOSFETs and also in Si FinFETs The contents also include a detailed investigation of the gate insulator process dependence of BTI in differently processed SiON and HKMG MOSFETs The book then goes on to discuss Reaction Diffusion RD model to estimate generation of new traps for DC and AC NBTI stress and Transient Trap Occupancy Model TTOM to estimate charge occupancy of generated traps and their contribution to BTI degradation Finally a comprehensive NBTI modeling framework including TTOM enabled RD model and hole trapping to predict time evolution of BTI degradation and recovery during and after DC stress for different stress and recovery biases and temperature during consecutive arbitrary stress and recovery cycles and during AC stress at different frequency and duty cycle The contents of this book should prove useful to academia and professionals alike

Design, Simulation and Construction of Field Effect Transistors Dhanasekaran Vikraman,Hyun-Seok Kim,2018-07-18 In recent years research on microelectronics has been specifically focused on the proposition of efficient alternative methodologies and materials to fabricate feasible integrated circuits This book provides a general background of thin film transistors and their simulations and constructions The contents of the book are broadly classified into two topics design and simulation of FETs and construction of FETs All the authors anticipate that the provided chapters will act as a single source of reference for the design simulation and construction of FETs This edited book will help microelectronics researchers with their endeavors and would be a great addition to the realm of semiconductor physics Two-dimensional Simulation and Design of High Electron Mobility Transistors Adele Hwei Ting Kam,1999

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