

DO-254 for the FPGA Designer

By: Dagan White

The standard that governs the design of avionic components and systems, DO-254, is one of the most poorly understood but widely applicable standards in the avionic industry. While information on the general aspects of the standard is easy to obtain, the details of exactly how to implement the standard are sketchy. And once an entity develops a process that achieves compliance, the details of how compliance was achieved become part of the intellectual property of that entity. This white paper focuses on the details of developing a DO-254 compliant process for the design of FPGAs.

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Darrin M Hanna, Richard E Haskell



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designed for software developers, software engineers, computer engineers, executives, professionals and researchers. Civil Aircraft Electrical Power System Safety Assessment Peng Wang,2017-06-12 Civil Aircraft Electrical Power System Safety Assessment Issues and Practices provides guidelines and methods for conducting a safety assessment process on civil airborne systems and equipment. As civil aircraft electrical systems become more complicated, electrical wiring failures have become a huge concern in industry and government, especially on aging platforms. There have been several accidents, most recently battery problems on the Boeing 777, with some of these having a relationship to wiring and power generation. Featuring a case study on the continuous safety assessment process of the civil airborne electrical power system, this book addresses problems, issues, and troubleshooting techniques such as single event effects, SEE, the failure effects of electrical wiring, interconnection systems, EWIS, formal theories, and safety analysis methods in civil aircrafts. Introduces how to conduct assignment of development assurance levels for the electrical power system. Includes safety assessments of aging platforms and their respective Electrical Wiring Interconnection System, EWIS. Features material on failure mechanisms for wiring systems and discussion of Failure Modes and Effects Analysis, FMEA, sustainment. **Designing with Xilinx® FPGAs**

Sanjay Churiwala,2016-10-20 This book helps readers to implement their designs on Xilinx FPGAs. The authors demonstrate how to get the greatest impact from using the Vivado Design Suite, which delivers a SoC strength, IP centric and system centric next generation development environment that has been built from the ground up to address the productivity bottlenecks in system level integration and implementation. This book is a hands on guide for both users who are new to FPGA designs as well as those currently using the legacy Xilinx tool set, ISE, but are now moving to Vivado. Throughout the presentation, the authors focus on key concepts, major mechanisms for design entry, and methods to realize the most efficient implementation of the target design with the least number of iterations. Digital Design Using Digilent FPGA Boards

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Image Processing With Xilinx Devices Adam Taylor,2017-12-21 This book contains a compilation of a number of blogs from the MicroZed Chronicles which examines how to develop image processing systems using Xilinx FPGA and heterogeneous SoC The initial chapters are new and explain the principles behind image processing sensors and sensor selection parameters

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