Martin Schlager

Hardware-in-the-Loop Simulation

A Scalable, Component-based, Time-triggered Hardware-in-the-loop Simulation Framework

Hardware In The Loop Simulation A Scalable Component Based Time Triggered Hardware In The Loop Simulation Framework

Rómulo Antão

Hardware In The Loop Simulation A Scalable Component Based Time Triggered Hardware In The Loop Simulation Framework:

Hardware-in-the-Loop Simulation Martin Schlager,2008 Safety critical real time systems must guarantee correct operation in all operational conditions even if these conditions are very unlikely to occur rare events Hardware in the Loop HiL simulation is a common validation technique of real time systems In an HiL simulation the environment of a System Under Test SUT is simulated by an assigned HiL simulator Thereby the SUT interacts with the HiL simulator in real time which necessitates a model of time and interfaces of the HiL simulator that are identical to the model of time and the interfaces of the SUT In this book an HiL simulation framework is proposed that allows predictable interaction of a distributed HiL simulator and an SUT This HiL simulation framework comprises configurable simulation components which are interconnected via a time triggered interaction mechanism Information flow between the HiL simulator and the SUT is strictly controlled by the progression of synchronized global time and bound to a priori known latency and jitter This book addresses researchers and engineers in safety critical domains such as the avionics or automotive industries **Automotive Applications of Hardware-in-the-Loop (HIL) Simulation** Adit Joshi,2019-08-13 Automotive Applications of Hardware in the Loop HIL Simulation shines a light on HIL simulation testing methodology commonly used in the automotive industry for conventional electrification and autonomy applications and can serve as an introductory resource for college students looking to join the automotive industry or experienced technical professionals who need a deeper understanding on what is HIL simulation what are its benefits and how can it be used in their respective organizations *Analytic Methods in Systems and Software Testing* Ron S. Kenett,Fabrizio Ruggeri,Frederick W. Faltin,2018-06-20 A comprehensive treatment of systems and software testing using state of the art methods and tools This book provides valuable insights into state of the art software testing methods and explains with examples the statistical and analytic methods used in this field Numerous examples are used to provide understanding in applying these methods to real world problems Leading authorities in applied statistics computer science and software engineering present state of the art methods addressing challenges faced by practitioners and researchers involved in system and software testing Methods include machine learning Bayesian methods graphical models experimental design generalized regression and reliability modeling Analytic Methods in Systems and Software Testing presents its comprehensive collection of methods in four parts Part I Testing Concepts and Methods Part II Statistical Models Part III Testing Infrastructures and Part IV Testing Applications It seeks to maintain a focus on analytic methods while at the same time offering a contextual landscape of modern engineering in order to introduce related statistical and probabilistic models used in this domain This makes the book an incredibly useful tool offering interesting insights on challenges in the field for researchers and practitioners alike Compiles cutting edge methods and examples of analytical approaches to systems and software testing from leading authorities in applied statistics computer science and software

engineering Combines methods and examples focused on the analytic aspects of systems and software testing Covers logistic regression machine learning Bayesian methods graphical models experimental design generalized regression and reliability models Written by leading researchers and practitioners in the field from diverse backgrounds including research business government and consulting Stimulates research at the theoretical and practical level Analytic Methods in Systems and Software Testing is an excellent advanced reference directed toward industrial and academic readers whose work in systems and software development approaches or surpasses existing frontiers of testing and validation procedures It will also be valuable to post graduate students in computer science and mathematics **Type-2 Fuzzy Logic** Rómulo Antão,2017-07-23 This book focuses on a particular domain of Type 2 Fuzzy Logic related to process modeling and control applications It deepens readers understanding of Type 2 Fuzzy Logic with regard to the following three topics using simpler methods to train a Type 2 Takagi Sugeno Fuzzy Model using the principles of Type 2 Fuzzy Logic to reduce the influence of modeling uncertainties on a locally linear n step ahead predictor and developing model based control algorithms according to the Generalized Predictive Control principles using Type 2 Fuzzy Sets Throughout the book theory is always complemented with practical applications and readers are invited to take their learning process one step farther and implement their own applications using the algorithms source codes provided As such the book offers avaluable referenceguide for allengineers and researchers in the field ofcomputer science who are interested in intelligent systems rule based systems and modeling uncertainty *Software Technologies for Embedded and Ubiquitous Systems* Roman Obermaisser,Yunmook Nah,Peter Puschner,Franz J. Rammig,2007-09-17 This book constitutes the thoroughly refereed post proceedings of the 5th IFIP WG 10 2 International Workshop on Software Technologies for Future Embedded and Ubiquitous Systems SEUS 2007 held in conjunction with ISORC 2007 the 10th IEEE International Symposium on Object component service oriented Real time Distributed Computing Coverage includes ubiquitous computing frameworks validation of embedded and ubiquitous systems and ubiquitous computing applications **Ambient Intelligence** Andreas Braun,Reiner Wichert,Antonio Maña,2017-04-03 This book constitutes the refereed proceedings of the 13th European Conference on Ambient Intelligence AmI 2017 held in Malaga Spain in April 2017 The 16 revised full papers presented together with 4 short papers and 1 keynote paper were carefully reviewed and selected from 48 submissions The papers cover topics such as Enabling technologies methods and platforms objectives and approaches of ambient intelligence and internet of things from information design to interaction and experience design and application areas of AmI and IoT **An Open Framework for Highly Concurrent Hardware-in-the-loop Simulation** Ryan C. Underwood,2007 Hardware in the loop HIL simulation is becoming a significant tool in prototyping complex highly available systems The HIL approach allows an engineer to build a physical system incrementally by enabling real components of the system to seamlessly interface with simulated components It also permits testing of hardware prototypes of components that would be extremely costly to test in the deployed environment Key issues

are the ability to wrap the systems of equations such as Partial Differential Equations describing the deployed environment into real time software models provide low synchronization overhead between the hardware and software and reduce reliance on proprietary platforms This thesis introduces an open source HIL simulation framework that can be ported to any standard Unix like system on any shared memory multiprocessor computer requires minimal operating system scheduler controls provides a soft real time guarantee for any constituent simulation that does likewise enables an asynchronous user interface and allows for an arbitrary number of secondary control components Abstract leaf iii *The Time-based Approach to Embedded Programming* Judith Liebman,2002 **Enhancing Embedded Systems Simulation** Christian Köhler,2011-03-29 Christian K hler covers the connection between C and simulation the interface abstraction as well as the analysis and optimization of coupling systems with the Chip Hardware in the Loop Simulation CHILS approach He develops the hardware to simulation coupling system with a focus on less hardware effort the capabilities to couple with different simulation environments and the efficiency of coupling Furthermore the author presents existing concepts to simulate complex systems and compares them with the new approach Designing Time Efficient Real Time Hardware in the Loop Simulation Using Input Profile Temporal Compression Sourindu Chatterjee,2017 *Development of a Hardware-in-the-loop Analysis Framework for Advanced Its Applications* Matthew Stephen Roe,2009 As Intelligent Transportation Systems ITS become more prevalent there is a need for a system capable of the rigorous evaluation of new ITS strategies for a wide variety of applications Pre deployment testing and fine tuning of the system performance evaluation and alternatives analysis are all potential benefits that could be gained through the evaluation of ITS Simulation an increasingly popular tool for transportation analysis would seem an ideal solution to this problem as it allows for the consideration of many scenarios that may be improbable or impossible to observe in the field Also simulation provides a framework that allows for the application of rigorous analysis techniques to the output data providing an accurate and statistically significant conclusion The difficulty is that many ITS strategies are difficult or impossible to implement in a simulated environment The rapid nature of technology development and the complicated nature of many ITS solutions are difficult to emulate in simulation models Furthermore the emulation of a particular ITS solution is not guaranteed to provide the same result that the physical system would were it subject to the same inputs This study seeks to establish a framework for the analysis of advanced ITS applications through the use of Hardware in the Loop Simulation HILS which provides a procedure for interfacing simulation models with real world hardware to conduct analysis This solution provides the benefits of both advanced ITS evaluation and simulation for powerful and accurate analysis A framework is established that includes all the steps of the modeling process including construction validation calibration and output analysis This ensures that the process surrounding the HILS implementation is valid so that the results of the evaluation are accurate and defendable Finally a case study of the application of the developed framework to the evaluation a real world implementation of an advanced ITS application SCATS

in this case is considered The effectiveness of the framework in creating and evaluating a corridor using a simulation model wed to real world hardware is shown The results of the analysis show the power of this method when correctly applied and demonstrate where further analysis could expand upon the proposed procedure **Development of HiLS and Paramics/VS-PLUS Integration** ,2008 *Advances in Desktop Hardware-in-the-loop Simulation* Herbert Hanselmann,Society of Automotive Engineers,1997 **Hardware in the Loop Simulation for Industrial Printers** Ricardo Nieto Rodríguez,2019 Hardware in the loop simulation has been used in the last years in the automotive navigation and aerospace industry to eliminate iterations in the design face of hardware This process consists in 4 main parts The controller model or Algorithm model of the plant embedded hardware and the real time processor to generate analog digital I O and communications The aim of this project is to first model a subsystem plant that is essential in all the printers and generates a lot of problems which is the ink delivery system Second objective of the thesis is to integrate the 4 parts of the HIL running simulations to prove feasibility of the model and comparing it versus reality The Design of a Real-Time Hardware-in-the-Loop Simulation Platform with Wind Effects □□□,2006 **Waveform Relaxation Based Hardware-in-the-loop Simulation** Mohammad (Monty) Goulkhah,2016 *Equipment Based on the Hardware in the Loop (HIL) Concept to Test Automation Equipment Using Plant Simulation* Eduardo Moreira,2011 Equipment Based on the Hardware in the Loop HIL Concept to Test Automation Equipment Using Plant Simulation *Hardware-in-the-Loop Real-Time Simulations and Verifications of Static Synchronous Compensators* Yu-Cheng Chang,2018 *Hardware-in-loop Simulation Using M68HC12* Deepthi B. Teegala,University of Hartford. College of Engineering, Technology, and Architecture,2011 **Development of a Low-cost Hardware-in-loop Simulator for Control Systems Experiments** David S. Bowden,2019 Students learn and retain knowledge better through experiential learning In mechatronics engineering education experience is mostly gained through experimentation in the laboratory Unfortunately laboratory equipment is large and expensive physical space to store the equipment is expensive and the technical staff required to keep it in operating condition is expensive but universities more than ever must find ways to reduce capital expenditures One method under investigation is the use of small inexpensive take home kits for mechatronics engineering students In previous usage these kits typically consist of a microprocessor development board with a development environment and enough peripherals to conduct simple experiments This type of kit is suitable for an introduction to microprocessors but seldom more than that The use of model based design MBD is becoming standard in industry due to its ability to provide simulation results during earlier phases of product development MBD allows verification to begin at an earlier development stage although at the cost of greater code size and slower execution rates than hand coded software MBD includes hardware in loop HIL testing where a completed controller can be tested against a model of the intended target in real time HIL testing allows controller implementations to be fully validated before they are used with prototype target systems reducing risk increasing

quality and shortening development time Unfortunately HIL systems used in industry are large expensive and complex which reduces their utility for the classroom This work combines trends in academia and industry to develop a small low cost take home kit that uses MBD throughout to create a HIL tester for a Quanser inverted pendulum experiment The kit uses two ST Microelectronics STM32F446RE Nucleo development boards one acting as a HIL and running a Simulink model of the pendulum the other acting as a controller and running a Simulink model of the student developed control algorithm Other components include cabling and a 3D printed case The total cost of the take home kit was estimated at 70 less than the cost of a textbook The proposed take home kit was validated for both controller performance and HIL performance HIL performance was assessed by comparing the performance of the kit controller with Quanser pendulum to the performance of the Quanser controller with Quanser pendulum and also to the performance of the kit controller in simulation with the HIL Results show that the difference between HIL simulation and physical pendulum is approximately the same as the difference between pendulum performance using the Quanser controller compared against pendulum performance using the kit controller Loop time for the HIL model to achieve real time simulation was targeted at 0 1 ms but testing showed that 0 5 ms loop time resulted in a more accurate simulation Using the proposed take home kit students can develop simulate and test algorithms with the components included and the developed control algorithm can then be demonstrated with an inverted pendulum in the classroom Providing development kits to each student in the classroom can reduce the need for laboratory time and allow fewer inverted pendulums to be shared among more students Since the HIL tester is designed using MBD tools it can be updated to simulate inverted pendulums with different physical characteristics by changing the parameters in the model It can also be programmed with different models so that it could simulate other control system experiments although this work only examines the inverted pendulum

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