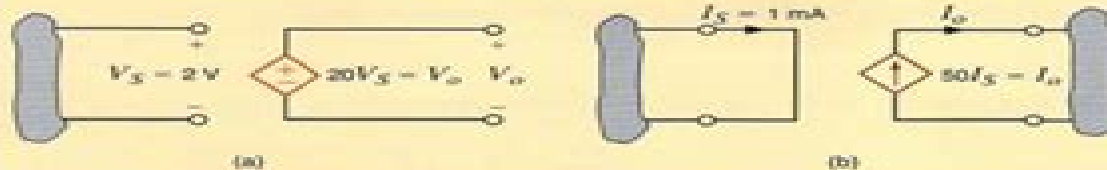


Given the two networks shown in Fig. 1.17, we wish to determine the outputs.

In Fig. 1.17a the output voltage is  $V_o = \mu V_s$  or  $V_o = 20 V_s = (20)(2 \text{ V}) = 40 \text{ V}$ . Note that the output voltage has been amplified from 2 V at the input terminals to 40 V at the output terminals; that is, the circuit is a voltage amplifier with an amplification factor of 20.



In Fig. 1.17b, the output current is  $I_o = \beta I_s = (50)(1 \text{ mA}) = 50 \text{ mA}$ ; that is, the circuit has a current gain of 50, meaning that the output current is 50 times greater than the input current.

### EXAMPLE 1.5

#### SOLUTION

← Figure 1.17  
Circuits for Example 1.5.

## Learning ASSESSMENT

**E1.4** Determine the power supplied by the dependent sources in Fig. E1.4.

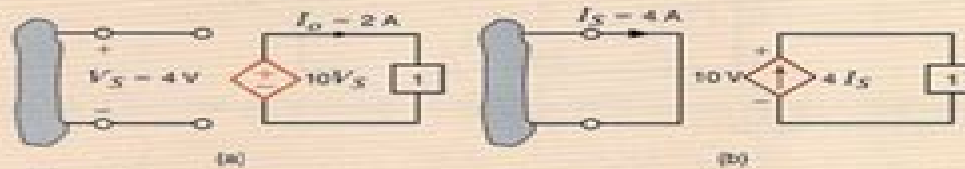
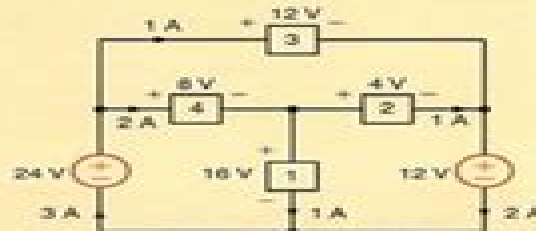


Figure E1.4

#### ANSWER:

- (a) Power supplied = 80 W;  
(b) power supplied = 160 W.

Calculate the power absorbed by each element in the network of Fig. 1.18. Also verify that Tellegen's theorem is satisfied by this network.



### EXAMPLE 1.6

← Figure 1.18  
Circuit used in Example 1.6.

Let's calculate the power absorbed by each element using the sign convention for power.

$$\begin{aligned} P_1 &= (16)(1) = 16 \text{ W} \\ P_2 &= (4)(1) = 4 \text{ W} \\ P_3 &= (12)(1) = 12 \text{ W} \end{aligned}$$

#### SOLUTION

# Basic Engineering Circuit Analysis 9th Solutions

**Gerald J. Kirwin, Stephen E. Grodzinsky**



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