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Intel Fpga Sdk For Opencl Altera

De-Shuang Huang,Bo Li,Haiming Chen,Chuanlei Zhang

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Evolving OpenMP for Evolving Architectures Bronis R. de Supinski,Pedro Valero-Lara,Xavier Martorell,Sergi Mateo Bellido,Jesus Labarta,2018-09-19 This book constitutes the proceedings of the 14th International Workshop on Open MP IWOMP 2018 held in Barcelona Spain in September 2018 The 16 full papers presented in this volume were carefully reviewed and selected for inclusion in this book The papers are organized in topical sections named best paper loops and OpenMP OpenMP in heterogeneous systems OpenMP improvements and innovations OpenMP user experiences applications and tools and tasking evaluations *Advances in Parallel & Distributed Processing, and Applications* Hamid R. Arabnia,Leonidas Deligiannidis,Michael R. Grimaila,Douglas D. Hodson,Kazuki Joe,Masakazu Sekijima,Fernando G. Tinetti,2021-10-18 The book presents the proceedings of four conferences The 26th International Conference on Parallel and Distributed Processing Techniques and Applications PDPTA 20 The 18th International Conference on Scientific Computing CSC 20 The 17th International Conference on Modeling Simulation and Visualization Methods MSV 20 and The 16th International Conference on Grid Cloud and Cluster Computing GCC 20 The conferences took place in Las Vegas NV USA July 27 30 2020 The conferences are part of the larger 2020 World Congress in Computer Science Computer Engineering Includes the research tracks Parallel and Distributed Processing Scientific Computing Modeling Simulation and Visualization and Grid Cloud and Cluster Computing Features papers from PDPTA 20 CSC 20 MSV 20 and GCC 20 *Domain-Specific Computer Architectures for Emerging Applications* Chao Wang,2024-06-04 With the end of Moore s Law domain specific architecture DSA has become a crucial mode of implementing future computing architectures This book discusses the system level design methodology of DSAs and their applications providing a unified design process that guarantees functionality performance energy efficiency and real time responsiveness for the target application DSAs often start from domain specific algorithms or applications analyzing the characteristics of algorithmic applications such as computation memory access and communication and proposing the heterogeneous accelerator architecture suitable for that particular application This book places particular focus on accelerator hardware platforms and distributed systems for various novel applications such as machine learning data mining neural networks and graph algorithms and also covers RISC V open source instruction sets It briefly describes the system design methodology based on DSAs and presents the latest research results in academia around domain specific acceleration architectures Providing cutting edge discussion of big data and artificial intelligence scenarios in contemporary industry and typical DSA applications this book appeals to industry professionals as well as academicians researching the future of computing in these areas **Advanced Intelligent Computing Technology and Applications** De-Shuang Huang,Bo Li,Haiming Chen,Chuanlei Zhang,2025-07-21 The 12 volume set CCIS 2564 2575 together with the 28 volume set LNCS LNAI LNBI 15842 15869 constitutes the refereed proceedings of the 21st International Conference on Intelligent Computing ICIC 2025 held in Ningbo China during July 26 29 2025 The 523 papers presented in these

proceedings books were carefully reviewed and selected from 4032 submissions This year the conference concentrated mainly on the theories and methodologies as well as the emerging applications of intelligent computing Its aim was to unify the picture of contemporary intelligent computing techniques as an integral concept that highlights the trends in advanced computational intelligence and bridges theoretical research with applications Therefore the theme for this conference was Advanced Intelligent Computing Technology and Applications **Euro-Par 2019: Parallel Processing** Ramin Yahyapour,2019-08-19 This book constitutes the proceedings of the 25th International Conference on Parallel and Distributed Computing Euro Par 2019 held in G ttingen Germany in August 2019 The 36 full papers presented in this volume were carefully reviewed and selected from 142 submissions They deal with parallel and distributed computing in general focusing on support tools and environments performance and power modeling prediction and evaluation scheduling and load balancing high performance architectures and compilers data management analytics and deep learning cluster and cloud computing distributed systems and algorithms parallel and distributed programming interfaces and languages multicore and manycore parallelism theory and algorithms for parallel computation and networking parallel numerical methods and applications accelerator computing algorithms and systems for bioinformatics and algorithms and systems for digital humanities **Algorithms and Architectures for Parallel Processing** Shadi Ibrahim,Kim-Kwang Raymond Choo,Zheng Yan,Witold Pedrycz,2017-08-09 This book constitutes the proceedings of the 17th International Conference on Algorithms and Architectures for Parallel Processing ICA3PP 2017 held in Helsinki Finland in August 2017 The 25 full papers presented were carefully reviewed and selected from 117 submissions They cover topics such as parallel and distributed architectures software systems and programming models distributed and network based computing big data and its applications parallel and distributed algorithms applications of parallel and distributed computing service dependability and security in distributed and parallel systems service dependability and security in distributed and parallel systems performance modeling and evaluation This volume also includes 41 papers of four workshops namely the 4th International Workshop on Data Text Web and Social Network Mining DTWSM 2017 the 5th International Workshop on Parallelism in Bioinformatics PBio 2017 the First International Workshop on Distributed Autonomous Computing in Smart City DACSC 2017 and the Second International Workshop on Ultrascale Computing for Early Researchers UCER 2017 Design of FPGA-Based Computing Systems with OpenCL Hasitha Muthumala Waidyasooriya,Masanori Hariyama,Kunio Uchiyama,2017-10-24 This book provides wide knowledge about designing FPGA based heterogeneous computing systems using a high level design environment based on OpenCL Open Computing language which is called OpenCL for FPGA The OpenCL based design methodology will be the key technology to exploit the potential of FPGAs in various applications such as low power embedded applications and high performance computing By understanding the OpenCL based design methodology readers can design an entire FPGA based computing system more easily compared to the conventional HDL based design because OpenCL for

FPGA takes care of computation on a host data transfer between a host and an FPGA computation on an FPGA with a capable of accessing external DDR memories In the step by step way readers can understand followings how to set up the design environment how to write better codes systematically considering architectural constraints how to design practical applications *Efficient Hardware Acceleration on SoC-FPGA Using OpenCL* Susmitha Gogineni,2017 Field Programmable Gate Arrays FPGAs are taking over the conventional processors in the field of High Performance computing With the advent of FPGA architectures and High level synthesis tools FPGAs can now be easily used to accelerate computationally intensive applications like e g AI and Cognitive computing One of the advantages of raising the level of hardware design abstraction is that multiple configurations with unique properties i e area performance and power can be automatically generated without the need to re write the input description This is not possible when using traditional low level hardware description languages like VHDL or Verilog This thesis deals with this important topic and accelerates multiple computationally intensive applications amiable to hardware acceleration and proposes a fast heuristic Design Space Exploration method to find dominant design solutions quickly In particular in this work we developed different computationally intensive applications in OpenCL and mapped them onto a heterogeneous SoC FPGA A Genetic Algorithm GA based meta heuristics that does automatic Design Space Exploration DSE on these applications was also developed as GA has shown in the past to lead to good results in multi objective optimization problems like this one The developed explorer automatically inserts a set of control knobs into the OpenCL behavioral description e g to control how to synthesize loops unroll or not and to replicate Compute Units CUs By tuning the these control attributes with possible values thousands of different micro architecture configurations can be obtained Thus an exhaustive search is not feasible and other heuristics are needed Each configuration is compiled using Altera OpenCL SDK tool and executed on Terasic DE1 SoC FPGA board platform to record the corresponding performance and logic utilization In order to measure the quality of the proposed GA based heuristic each application is explored exhaustively taking multiple days to finish for smaller designs to find the dominant optimal solutions Pareto Optimal Designs For complex and larger designs exploring the entire design space exhaustively is not feasible due to very large design space The comparison is quantified by using metrics like Dominance Average Distance from Reference Set ADRS and run time speed up showing that our proposed heuristics lead to very good results at a fraction of the time of the exhaustive search *Beginning FPGA: Programming Metal* Aiken Pang,Peter Membrey,2016-12-23 Use Arrow s affordable and breadboard friendly FPGA development board BeMicro MAX 10 to create a light sensor temperature sensor motion sensor and the KITT car display from Knight Rider You don t need an electronics engineering degree or even any programming experience to get the most out of Beginning FPGA Programming Metal Just bring your curiosity and your Field Programmable Gate Array This book is for those who have tinkered with Arduino or Raspberry Pi and want to get more hands on experience with hardware or for those new to electronics who just want todive in You ll learn the theory behind FPGAs

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     Advanced HW/SW Embedded System for Designers 2018 Tommy Klevin,Lennart Lindh,2018-11-10 Highlights Learn by doing real designs on FPGA boards Theory connected to practical training Microcontroller architectures and FPGA technology HW SW IP component design and simple device driver design Basic multiprocessor system Integration of a hardware based RTOS accelerator Free advanced Intel FPGA former Altera HW and SW development tools for the training tasks Recommended entry requirements are Low Level C Programming for Designers 2018 Amazon and basic VHDL programming Uses DE10 Lite board   Using OpenCL to Implement Finite Deformation Elasticity Model on FPGA Xiangyu James Zhang,2015 The finite deformation elasticity is a theory that describes the capability of the elastic materials undergoing deformations The finite element method FEM is constructed to solve problems based on this theory The FEM method subdivides the whole problem domain into simpler parts and obtains the approximate results by connecting these simpler parts over subdomains Solving these problems in real life situations require significantly high computing power highlighting the need for high performance computational devices in order to accelerate the calculation process Altera announced industry s first OpenCL framework for FPGA devices This tool combines the FPGA with the OpenCL standard to

construct powerful system acceleration In this thesis an OpenCL solution for finite deformation elasticity is implemented on Altera manufactured Cyclone V SoC development kit The Cyclone V SoC contains the hard processor system with integrated ARM processor and FPGA allowing for the host program of the OpenCL application to be executed on the ARM processor and use FPGA s parallel performance capability to run the OpenCL kernel The OpenCL kernel is developed to concurrently calculate all the deformation gradient tensors for all elements and a comparison benchmark is conducted to compare the execution time and power consumption between FPGA and GPU setups The results show that FPGA is 3 5 times faster than GPU and consume significantly lower power

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