

Figure 8.1 Common-emitter fixed-bias configuration.

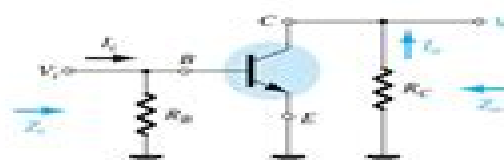
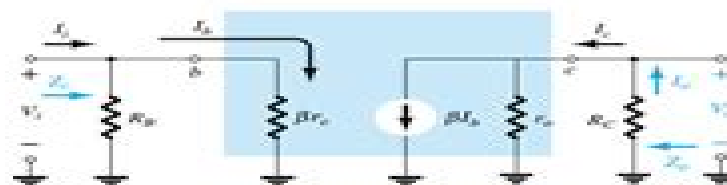


Figure 8.2 Network of Figure 8.1 following the removal of the effects of  $V_{CC}$ ,  $C_1$ , and  $C_2$ .

Figure 8.3 Substituting the  $r_e$  model into the network of Fig. 8.2.



The next step is to determine  $\beta$ ,  $r_e$ , and  $r_o$ . The magnitude of  $\beta$  is typically obtained from a specification sheet or by direct measurement using a curve tracer or transistor testing instrument. The value of  $r_e$  must be determined from a dc analysis of the system, and the magnitude of  $r_o$  is typically obtained from the specification sheet or characteristics. Assuming that  $\beta$ ,  $r_e$ , and  $r_o$  have been determined will result in the following equations for the important two-port characteristics of the system.

$Z_i$ : Figure 8.3 clearly reveals that

$$Z_i = R_B \parallel \beta r_e \quad \text{ohms} \quad (8.1)$$

For the majority of situations  $R_B$  is greater than  $\beta r_e$  by more than a factor of 10 (recall from the analysis of parallel elements that the total resistance of two parallel resistors is always less than the smallest and very close to the smallest if one is much larger than the other), permitting the following approximation:

$$Z_i \approx \beta r_e \quad R_B \gg \beta r_e \quad \text{ohms} \quad (8.2)$$

$Z_o$ : Recall that the output impedance of any system is defined as the impedance  $Z_o$  determined when  $V_i = 0$ . For Fig. 8.3, when  $V_i = 0$ ,  $I_i = I_b = 0$ , resulting in an open-circuit equivalence for the current source. The result is the configuration of Fig. 8.4.

$$Z_o = R_C \parallel r_o \quad \text{ohms} \quad (8.3)$$

If  $r_o \approx 10 R_C$ , the approximation  $R_C \parallel r_o \approx R_C$  is frequently applied and

$$Z_o \approx R_C \quad r_o \gg 10 R_C \quad (8.4)$$

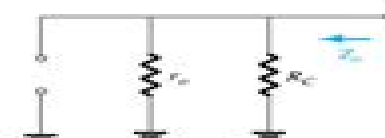


Figure 8.4 Determining  $Z_o$  for the network of Fig. 8.3.

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**Clemens Wendtner**



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## **Table of Contents Introduction To Circuit Analysis 7th Edition By Boylestad Solutions**

1. Understanding the eBook Introduction To Circuit Analysis 7th Edition By Boylestad Solutions
  - The Rise of Digital Reading Introduction To Circuit Analysis 7th Edition By Boylestad Solutions
  - Advantages of eBooks Over Traditional Books
2. Identifying Introduction To Circuit Analysis 7th Edition By Boylestad Solutions
  - Exploring Different Genres
  - Considering Fiction vs. Non-Fiction
  - Determining Your Reading Goals
3. Choosing the Right eBook Platform
  - Popular eBook Platforms
  - Features to Look for in an Introduction To Circuit Analysis 7th Edition By Boylestad Solutions
  - User-Friendly Interface
4. Exploring eBook Recommendations from Introduction To Circuit Analysis 7th Edition By Boylestad Solutions
  - Personalized Recommendations
  - Introduction To Circuit Analysis 7th Edition By Boylestad Solutions User Reviews and Ratings
  - Introduction To Circuit Analysis 7th Edition By Boylestad Solutions and Bestseller Lists

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  - Interactive Elements Introduction To Circuit Analysis 7th Edition By Boylestad Solutions
8. Staying Engaged with Introduction To Circuit Analysis 7th Edition By Boylestad Solutions
  - Joining Online Reading Communities
  - Participating in Virtual Book Clubs
  - Following Authors and Publishers Introduction To Circuit Analysis 7th Edition By Boylestad Solutions
9. Balancing eBooks and Physical Books Introduction To Circuit Analysis 7th Edition By Boylestad Solutions
  - Benefits of a Digital Library
  - Creating a Diverse Reading Collection Introduction To Circuit Analysis 7th Edition By Boylestad Solutions
10. Overcoming Reading Challenges
  - Dealing with Digital Eye Strain
  - Minimizing Distractions
  - Managing Screen Time
11. Cultivating a Reading Routine Introduction To Circuit Analysis 7th Edition By Boylestad Solutions
  - Setting Reading Goals Introduction To Circuit Analysis 7th Edition By Boylestad Solutions
  - Carving Out Dedicated Reading Time
12. Sourcing Reliable Information of Introduction To Circuit Analysis 7th Edition By Boylestad Solutions
  - Fact-Checking eBook Content of Introduction To Circuit Analysis 7th Edition By Boylestad Solutions
  - Distinguishing Credible Sources
13. Promoting Lifelong Learning

- Utilizing eBooks for Skill Development
- Exploring Educational eBooks

### 14. Embracing eBook Trends

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