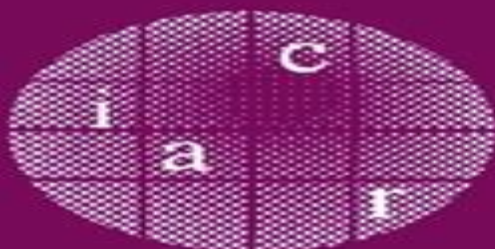


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Marc Joye
Jean-Jacques Quisquater (Eds.)

Cryptographic Hardware and Embedded Systems – CHES 2004

6th International Workshop
Cambridge, MA, USA, August 2004
Proceedings



 Springer

Cryptographic Hardware And Embedded Systems Ches 2004 6th International Workshop Cambridge Ma Us

Yijin Wang



Cryptographic Hardware And Embedded Systems Ches 2004 6th International Workshop Cambridge Ma Us:

Cryptographic Hardware and Embedded Systems - CHES 2004 Marc Joye, Jean-Jaques Quisquater, 2004-07-08 These are the proceedings of CHES 2004 the 6th Workshop on Cryptographic Hardware and Embedded Systems For the first time the CHES Workshop was sponsored by the International Association for Cryptologic Research IACR This year the number of submissions reached a new record One hundred and twenty five papers were submitted of which 32 were selected for presentation Each submitted paper was reviewed by at least 3 members of the program committee We are very grateful to the program committee for their hard and efficient work in assembling the program We are also grateful to the 108 external referees who helped in the review process in their area of expertise In addition to the submitted contributions the program included three invited talks by Neil Gershenfeld Center for Bits and Atoms MIT about Physical Information Security by Isaac Chuang Medialab MIT about Quantum Cryptography and by Paul Kocher Cryptography Research about Physical Attacks It also included a rump session chaired by Christof Paar which featured informal talks on recent results As in the previous years the workshop focused on all aspects of cryptographic hardware and embedded system security We sincerely hope that the CHES Workshop series will remain a premium forum for intellectual exchange in this area

Cryptographic Hardware and Embedded Systems--CHES 2004, 2004 **Cryptographic Hardware and Embedded Systems - CHES 2004** Marc Joye, Jean-Jaques Quisquater, 2004-07-28 This book constitutes the refereed proceedings of the 6th International workshop on Cryptographic Hardware and Embedded Systems CHES 2004 held in Cambridge MA USA in August 2004 The 32 revised full papers presented were carefully reviewed and selected from 125 submissions The papers are organized in topical sections on side channels modular multiplication low resources implementation aspects collision attacks fault attacks hardware implementation and authentication and signatures

Cryptographic Hardware and Embedded Systems - CHES 2005 Josyula R. Rao, Berk Sunar, 2005-09-19 These are the proceedings of the 7th Workshop on Cryptographic Hardware and Embedded Systems CHES 2005 held in Edinburgh Scotland from August 29 to September 1 2005

Cryptographic Hardware and Embedded Systems - CHES 2004 Marc Joye, Jean-Jaques Quisquater, 2004-07-28 This book constitutes the refereed proceedings of the 6th International workshop on Cryptographic Hardware and Embedded Systems CHES 2004 held in Cambridge MA USA in August 2004 The 32 revised full papers presented were carefully reviewed and selected from 125 submissions The papers are organized in topical sections on side channels modular multiplication low resources implementation aspects collision attacks fault attacks hardware implementation and authentication and signatures

Embedded Security in Cars Kerstin Lemke, Christof Paar, Marko Wolf, 2006-03-28 Most innovations in the car industry are based on software and electronics and IT will soon constitute the major production cost factor It seems almost certain that embedded IT security will be crucial for the next generation of applications Yet whereas software safety has become a relatively well established field the protection of automotive IT systems against manipulation or intrusion has only recently

started to emerge Lemke Paar and Wolf collect in this volume a state of the art overview on all aspects relevant for IT security in automotive applications After an introductory chapter written by the editors themselves the contributions from experienced experts of different disciplines are structured into three parts Security in the Automotive Domain describes applications for which IT security is crucial like immobilizers tachographs and software updates Embedded Security Technologies details security technologies relevant for automotive applications e g symmetric and asymmetric cryptography and wireless security Business Aspects of IT Systems in Cars shows the need for embedded security in novel applications like location based navigation systems and personalization The first book in this area of fast growing economic and scientific importance it is indispensable for both researchers in software or embedded security and professionals in the automotive industry

Understanding Logic Locking Kimia Zamiri Azar,Hadi Mardani Kamali,Farimah Farahmandi,Mark Tehranipoor,2023-09-22 This book demonstrates the breadth and depth of IP protection through logic locking considering both attacker adversary and defender designer perspectives The authors draw a semi chronological picture of the evolution of logic locking during the last decade gathering and describing all the DO s and DON Ts in this approach They describe simple to follow scenarios and guide readers to navigate identify threat models and design evaluation flow for further studies Readers will gain a comprehensive understanding of all fundamentals of logic locking

Handbook of Signal Processing Systems Shuvra S. Bhattacharyya,Ed F. Deprettere,Rainer Leupers,Jarmo Takala,2010-09-10 It gives me immense pleasure to introduce this timely handbook to the research development communities in the eld of signal processing systems SPS This is the rst of its kind and represents state of the arts coverage of research in this eld The driving force behind information technologies IT hinges critically upon the major advances in both component integration and system integration The major breakthrough for the former is undoubtedly the invention of IC in the 50 s by Jack S Kilby the Nobel Prize Laureate in Physics 2000 In an integrated circuit all components were made of the same semiconductor material Beginning with the pocket calculator in 1964 there have been many increasingly complex applications followed In fact processing gates and memory storage on a chip have since then grown at an exponential rate following Moore s Law Moore himself admitted that Moore s Law had turned out to be more accurate longer lasting and deeper in impact than he ever imagined With greater device integration various signal processing systems have been realized for many killer IT applications Further breakthroughs in computer sciences and Internet technologies have also catalyzed large scale system integration All these have led to today s IT revolution which has profound impacts on our lifestyle and overall prospect of humanity It is hard to imagine life today without mobiles or Internets The success of SPS requires a well concerted integrated approach from mul ple disciplines such as device design and application

Handheld Computing for Mobile Commerce: Applications, Concepts and Technologies Hu, Wen-Chen,Zuo, Yanjun,2010-02-28 This book looks at theory design implementation analysis and application of handheld computing under four themes handheld computing for mobile commerce handheld computing research and technologies

wireless networks and handheld mobile security and handheld images and videos Provided by publisher **Handbook of Information Security, Threats, Vulnerabilities, Prevention, Detection, and Management** Hossein Bidgoli, 2006-03-13

The Handbook of Information Security is a definitive 3 volume handbook that offers coverage of both established and cutting edge theories and developments on information and computer security The text contains 180 articles from over 200 leading experts providing the benchmark resource for information security network security information privacy and information warfare The British National Bibliography Arthur James Wells, 2004 **Cryptographic Hardware and Embedded Systems--CHES 2000 : Second International Workshop, Worcester, MA, USA, August 17-18, 2000 : Proceedings** CHES 2000, 2001 *Cryptographic Hardware and Embedded Systems - CHES 2000* Cetin K. Koc, Christof Paar, 2003-07-31

This book constitutes the thoroughly refereed post proceedings of the Second International Workshop on Cryptographic Hardware and Embedded Systems CHES 2000 held in Worcester MA USA in August 2000 The 25 revised full papers presented together with two invited contributions were carefully reviewed and selected from 51 submissions The papers are organized in topical sections on implementation of elliptic curve cryptosystems power and timing analysis attacks hardware implementation of block ciphers hardware architectures power analysis attacks arithmetic architectures physical security and cryptanalysis and new schemes and algorithms **Cryptographic Hardware and Embedded Systems - CHES 2000** Cetin K. Koc, Christof Paar, 2000-12-13

This book constitutes the thoroughly refereed post proceedings of the Second International Workshop on Cryptographic Hardware and Embedded Systems CHES 2000 held in Worcester MA USA in August 2000 The 25 revised full papers presented together with two invited contributions were carefully reviewed and selected from 51 submissions The papers are organized in topical sections on implementation of elliptic curve cryptosystems power and timing analysis attacks hardware implementation of block ciphers hardware architectures power analysis attacks arithmetic architectures physical security and cryptanalysis and new schemes and algorithms **Cryptographic Hardware and Embedded Systems - CHES 2008** Elisabeth Oswald, Pankaj Rohatgi, 2008 **Cryptographic Hardware and Embedded Systems -- CHES 2010** Stefan Mangard, Francois-Xavier Standaert, 2010 **Cryptographic Hardware and Embedded Systems** Cetin K. Koc, Christof Paar, 2003-07-31

This book constitutes the refereed proceedings of the First International Workshop on Cryptographic Hardware and Embedded Systems CHES 99 held in Worcester MA USA in August 1999 The 27 revised papers presented together with three invited contributions were carefully reviewed and selected from 42 submissions The papers are organized in sections on cryptographic hardware hardware architectures smartcards and embedded systems arithmetic algorithms power attacks true random numbers cryptographic algorithms on FPGAs elliptic curve implementations new cryptographic schemes and modes of operation *Cryptographic Hardware and Embedded Systems - CHES 2001* Cetin K. Koc, David Naccache, Christof Paar, 2003-06-30

These are the proceedings of CHES 2001 the third Workshop on Cryptographic Hardware and Embedded Systems The first two CHES Workshops were held in

Massachusetts and this was the first Workshop to be held in Europe. There was a large number of submissions this year and in response the technical program was extended to 2 1/2 days. As is evident by the papers in these proceedings many excellent submissions were made. Selecting the papers for this year's CHES was not an easy task and we regret that we had to reject several very interesting papers due to the lack of time. There were 66 submitted contributions this year of which 31 or 47% were selected for presentation. If we look at the number of submitted papers at CHES 99 42 papers and CHES 2001 51 papers we observe a steady increase. We interpret this as a continuing need for a workshop series which combines theory and practice for integrating strong security features into modern communications and computer applications. In addition to the submitted contributions Ross Anderson from Cambridge University UK and Adi Shamir from The Weizmann Institute Israel gave invited talks. As in previous years the focus of the workshop is on all aspects of cryptographic hardware and embedded system design. Of special interest were contributions that describe new methods for efficient hardware implementations and high speed software for embedded systems e.g. smart cards microprocessors DSPs etc. CHES also continues to be an important forum for new theoretical and practical findings in the important and growing field of side channel attacks.

Cryptographic Hardware and Embedded Systems Çetin K. Koç, Christof Paar, 1999. This book constitutes the refereed proceedings of the First International Workshop on Cryptographic Hardware and Embedded Systems CHES 99 held in Worcester MA USA in August 1999. The 27 revised papers presented together with three invited contributions were carefully reviewed and selected from 42 submissions. The papers are organized in sections on cryptographic hardware hardware architectures smartcards and embedded systems arithmetic algorithms power attacks true random numbers cryptographic algorithms on FPGAs elliptic curve implementations new cryptographic schemes and modes of operation.

Cryptographic Hardware and Embedded Systems -- CHES 2013 Guido Marco Bertoni, Jean-Sébastien Coron, 2013-07-19. This book constitutes the proceedings of the 15th International Workshop on Cryptographic Hardware and Embedded Systems CHES 2013 held in Santa Barbara CA USA in August 2013. The 27 papers presented were carefully reviewed and selected from 132 submissions. The papers are organized in the following topical sections: side channel attacks physical unclonable function lightweight cryptography hardware implementations and fault attacks efficient and secure implementations elliptic curve cryptography masking side channel attacks and countermeasures.

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