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Basic VLSI Design



Douglas A. Pucknell
Kamran Eshraghian



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Basic VLSI Design Douglas A. Pucknell, Kamran Eshraghian, 1985 **Basic VLSI Design** Douglas A. Pucknell, Kamran Eshraghian, 1994 AN INTRODUCTION TO VLSI DESIGN AND TESTING P. Brundavani, B. Abdul Raheem, Rachapudi Prabahakar, 2025-05-20 VLSI Design and Testing provides a concise yet comprehensive guide to the design analysis and testing of integrated circuits Covering key topics such as IC types Moore's Law MOSFET and CMOS fabrication and SOI technology the book builds a strong foundation in VLSI principles It explores the design flow CMOS logic gates layout techniques and both static and dynamic logic circuits Readers will also learn about circuit performance parameters scaling theory and subsystem design including adders shifters and comparators The book concludes with essential concepts in VLSI design styles FPGA gate array full custom and CMOS testing including fault models ATPG and BIST Ideal for students and professionals it blends theory with practical design strategies in modern VLSI systems Visit garuda.publishers.com **Basic VLSI Design** Douglas A. Pucknell, Kamran Eshraghian, 1988 **Study Guide to Basic VLSI Design** Douglas A. Pucknell, Kamran Eshraghian, 1985 **ELECTRICAL AND ELECTRONIC MEASUREMENTS** GOPAL KRISHNA BANERJEE, 2012-01-18 In this modern scientific world a thorough understanding of complex measurements and instruments is the need of the hour This book provides a comprehensive coverage of the concepts and principles of measurements and instrumentation and brings into focus the recent and significant developments in this field The book presents an exhaustive exposition of different types of measuring instruments and their applications in an easy to grasp manner It presents even the minute details of various measurement techniques and calibration methods which are the essential features of a measurement programme The book elaborates on the theoretical background and practical knowledge of different measuring instruments to make the students accustomed to these devices An in depth coverage of topics makes the text useful to somewhat more advanced courses and its elaborated methodology will help students meet the challenges in their career This book is ideally suitable for undergraduate students BE B Tech of Electrical Electronics and Instrumentation and Control disciplines of engineering It can be also used as reference book for the cable testing testing of instruments transformers testing of energy meters and measurement of physical variables **KEY FEATURES** Gives a number of chapter end review questions and numerical problems for practice Includes plenty of diagrams to clarify the concepts Contains about 250 problems and 200 solved examples for the benefit of the students VLSI CAD CHIPLUNKAR, NIRANJAN N., KOTARI, MANJUNATH, 2011-04-01 This well organised book presents the basics of VLSI along with important algorithms used by CAD tool designers It discusses general VLSI design styles layout design rules technology mapping in FPGAs and 3D FPGAs In addition the text describes three important steps in high level synthesis of VLSI namely partitioning scheduling and data path allocation besides logic synthesis which determines the gate level structure of circuits Finally the book gives a detailed account of physical synthesis where steps such as floorplanning placement routing and compaction are explained with

necessary algorithms This book is intended as a text for the undergraduate and postgraduate students of engineering Electrical and Electronics Engineering Electronics and Communication Engineering Computer Science and Engineering besides Instrumentation for their course on VLSI CAD In addition the book would also be extremely useful for professionals in this field KEY FEATURES Presents a variety of chip design tools Includes a fairly large number of algorithms Discusses VHDL and graph theory essential for VLSI CAD tool design Provides 100 questions selected from various university examination papers

SOLID STATE DEVICES NAIR, B. SOMANATHAN, DEEPA, S. R., 2018-11-01 Designed as a text for undergraduate students of engineering in Electrical Electronics and Computer Science and IT disciplines as well as undergraduate students B Sc of physics and electronics as also for postgraduate students of physics and electronics this compact and accessible text endeavours to simplify the theory of solid state devices so that even an average student will be able to understand the concepts with ease The authors Prof Somanathan Nair and Prof S R Deepa with their rich and long experience in teaching the subject provide a detailed discussion of such topics as crystal structures of semiconductor materials Miller indices energy band theory of solids energy level diagrams and mass action law Besides they give a masterly analysis of topics such as direct and indirect gap materials Fermi Dirac statistics electrons in semiconductors Hall effect PN junction diodes Zener and avalanche breakdowns Schottky barrier diodes bipolar junction transistors MOS field effect transistors Early effect Shockley diodes SCRs TRIAC and IGBTs In the Second Edition two new chapters on opto electronic devices and electro optic devices have been added The text has been thoroughly revised and updated A number of solved problems and objective type questions have been included to help students develop grasp of the contents This fully illustrated and well organized text should prove invaluable to students pursuing various courses in engineering and physics

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