

FEATURES

- Digitally programmable frequency and phase
- 12.65 mW power consumption at 3 V
- 0 MHz to 12.5 MHz output frequency range
- 28-bit resolution: 0.1 Hz at 25 MHz reference clock
- Sinusoidal, triangular, and square wave outputs
- 2.3 V to 5.5 V power supply
- No external components required
- 3-wire SPI interface
- Extended temperature range: -40°C to $+105^{\circ}\text{C}$
- Power-down option
- 10-lead MSOP package
- Qualified for automotive applications

APPLICATIONS

- Frequency stimulus/waveform generation
- Liquid and gas flow measurement
- Sensory applications: proximity, motion, and defect detection
- Line loss/attenuation
- Test and medical equipment
- Sweep/clock generators
- Time domain reflectometry (TDR) applications

GENERAL DESCRIPTION

The AD9833 is a low power, programmable waveform generator capable of producing sine, triangular, and square wave outputs. Waveform generation is required in various types of sensing, actuation, and time domain reflectometry (TDR) applications. The output frequency and phase are software programmable, allowing easy tuning. No external components are needed. The frequency registers are 28 bits wide; with a 25 MHz clock rate, resolution of 0.1 Hz can be achieved; with a 1 MHz clock rate, the AD9833 can be tuned to 0.004 Hz resolution.

The AD9833 is written to via a 3-wire serial interface. This serial interface operates at clock rates up to 40 MHz and is compatible with DSP and microcontroller standards. The device operates with a power supply from 2.3 V to 5.5 V.

The AD9833 has a power-down function (SLEEP). This function allows sections of the device that are not being used to be powered down, thus minimizing the current consumption of the part. For example, the DAC can be powered down when a clock output is being generated.

The AD9833 is available in a 10-lead MSOP package.

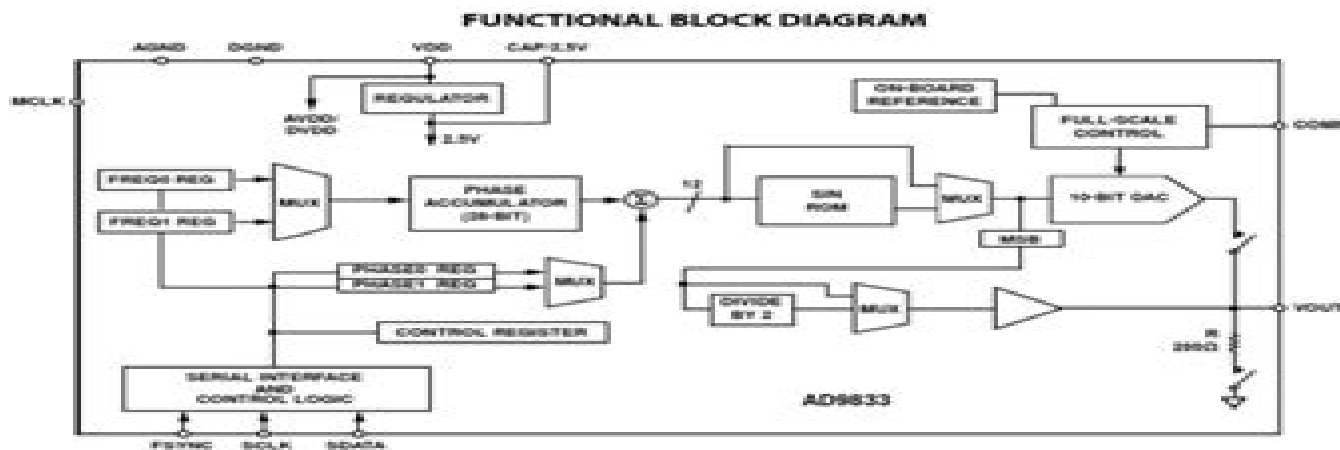


Figure 1.

Rev. E

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Analog-Digital Conversion Handbook, by the Engineering Staff of Analog Devices, Inc. Edited by Daniel H. Sheingold Analog Devices, inc,1972

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Practical Analog Design Techniques Walt Kester,1995

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Analog-digital Conversion Notes Analog Devices, inc,1977 This 1977 seminar edition is the first major revision of material previously published in the highly successful Analog Digital Conversion Handbook first published by Analog Devices Inc in 1972 and reprinted in 1976 This volume contains parts I and II of that book updated wherever specific converted products are referred to in order to reflect the revolution in cost size and in some cases performance brought about by the development of converters in integrated circuit and hybrid form Two entirely new chapters have been added to further reflect changes in the structure of the technological marketplace brought about by the availability of both converters and computers as true components Chapter II 2 is a consideration of the relationship between processes configurations and performance in miniature low cost converters Chapter I 4 considers the application of converters with parallel and serial digital interfaces microcomputers asynchronous serial data posts and proprietary integrated data conversion subsystem architectures a natural sequel to the wide ranging discussion of system considerations in other chapters Page i

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