英飞凌 32-Bit TriCore™ 架构



Controller Features

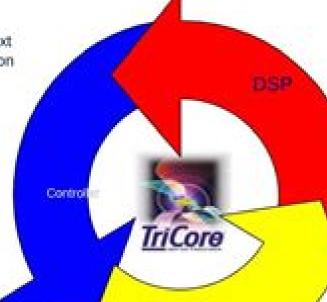
response (<180ns @ 150MHz)

 Fast context switch TC1.3 -> 2cycles ->½ context

- 16-bit and 32-bit instruction formats
- Bit manipulation unit
- Accumulated logical/compare
- Integrated peripheral support

DSP Features

- DSP addressing modes
- Zero overhead loop
- Saturation and Q-Math
- Overflow detection.
- Rounding



Processor

TriCore™ Architecture

Real time MCU + DSP Computational power + price/performance of RISC load/store architectures

First single-core 32-bit MCU-DSP

Optimized for real-time embedded systems

RISC Processor Features

2-bit load/store Harvard architecture with 128 bit instruction / 256 bit data bus width

- 16 address & 16 data registers.
- Super-scalar execution (3 parallel pipelines)
- Multiple instruction issuing (up to 3 instructions per clock)
- Single data-memory model
- Memory protection
- C/ C++ and RTOS support
- Tightly coupled FPU unit

Abstract 1 Introduction Infineon

Christian Schäffer

Abstract 1 Introduction Infineon:

Advances in Artificial Intelligence, Software and Systems Engineering Tareg Ahram, 2019-06-10 This book addresses emerging issues resulting from the integration of artificial intelligence systems in our daily lives It focuses on the cognitive visual social and analytical aspects of computing and intelligent technologies highlighting ways to improve the acceptance effectiveness and efficiency of said technologies Topics such as responsibility integration and training are discussed throughout The book also reports on the latest advances in systems engineering with a focus on societal challenges and next generation systems and applications for meeting them The book is based on two AHFE 2019 Affiliated Conferences on Artificial Intelligence and Social Computing and on Service Software and Systems Engineering which were jointly held on July 24 28 2019 in Washington DC USA Computer Safety, Reliability, and Security Alexander Romanovsky, Elena Troubitsyna, Ilir Gashi, Erwin Schoitsch, Friedemann Bitsch, 2019-09-02 This book constitutes the proceedings of the Workshops held in conjunction with SAFECOMP 2019 38th International Conference on Computer Safety Reliability and Security in September 2019 in Turku Finland The 32 regular papers included in this volume were carefully reviewed and selected from 43 submissions the book also contains two invited papers The workshops included in this volume are ASSURE 2019 7th International Workshop on Assurance Cases for Software Intensive Systems DECSoS 2019 14th ERCIM EWICS ARTEMIS Workshop on Dependable Smart Embedded and Cyber Physical Systems and Systems of Systems SASSUR 2019 8th International Workshop on Next Generation of System Assurance Approaches for Safety Critical Systems STRIVE 2019 Second International Workshop on Safety security and privacy In automotive systems WAISE 2019 Second International Workshop on Artificial Intelligence Safety Engineering
Digital Transformation in Semiconductor Manufacturing Sophia Keil, Rainer Lasch, Fabian Lindner, Jacob Lohmer, 2020-05-26 This open access book reports on cutting edge electrical engineering and microelectronics solutions to foster and support digitalization in the semiconductor industry Based on the outcomes of the European project iDev40 which were presented at the two first conference editions of the European Advances in Digital Transformation Conference EADCT 2018 and EADTC 2019 the book covers different multidisciplinary aspects related to digital transformation including technological and industrial developments as well as human factors research and applications Topics include modeling and simulation methods in semiconductor operations supply chain management issues employee training methods and workplaces optimization as well as smart software and hardware solutions for semiconductor manufacturing By highlighting industrially relevant developments and discussing open issues related to digital transformation the book offers a timely practice oriented guide to graduate students researchers and professionals interested in the digital transformation of manufacturing domains and work environments Computer Security - ESORICS 2003 Einar Snekkenes, 2003-09-29 This book constitutes the refereed proceedings of the 8th European Symposium on Research in Computer Security ESORICS 2003 held in Gjovik Norway in October 2003 The 19 revised full

papers presented were carefully reviewed and selected from 114 submissions Among the topics addressed are signature control access control key exchange broadcast protocols privacy preserving technologies attack analysis electronic voting identity control authentication security services smart card security formal security protocols analysis and intrusion Online Engineering and Society 4.0 Michael E. Auer, Kalvan Ram Bhimavaram, Xiao-Guang Yue, 2021-09-19 This book presents the general objective of the REV2021 conference which is to contribute and discuss fundamentals applications and experiences in the field of Online and Remote Engineering Virtual Instrumentation and other related new technologies like Cross Reality Data Science Big Data Internet of Things Industrial Internet of Things Industry 4 0 Cyber Security and M2M Smart Objects Nowadays online technologies are the core of most fields of engineering and the whole society and are inseparably connected for example with Internet of Things Industry 4 0 Industrial Internet of Things Cloud Technologies Data Science Cross Mixed Reality Remote Working Environments Online Biomedical Engineering to name only a few Since the first REV conference in 2004 we tried to focus on the upcoming use of the Internet for engineering tasks and the opportunities as well as challenges around it In a globally connected world the interest in online collaboration teleworking remote services and other digital working environments is rapidly increasing Another objective of the conference is to discuss guidelines and new concepts for engineering education in higher and vocational education institutions including emerging technologies in learning MOOCs MOOLs and Open Resources REV2021 on Online Engineering and Society 4 0 was the 17th in a series of annual events concerning the area of Remote Engineering and Virtual Instrumentation It has been organized in cooperation with the International Engineering and Technology Institute IETI as an online event from February 24 to 26 2021 Photonische Netze Christian Schäffer,2004 Proceedings of 3rd Electronics Packaging Technology Conference (EPTC 2000) Thiam Beng Lim, 2000 Topics covered by this title include packaging materials packaging trends thermal design and modelling solder joint metallurgy process and reliability modelling thermal characterization materials characterization techniques and assembly manufacturing technologies Meeting Abstracts Electrochemical Society, 2002 Ultra Clean Processing of Silicon Surfaces V Marc Heyns, Marc Meuris, Paul Mertens, 2001-01-02 UCPSS 2000 Data Integrated Circuit and System Design, 2003 Analysis and Modeling for Process Control, 2005 Conference Proceedings, 2000 Annual Symposium on Photomask Technology ,2002 2001 International Symposium on VLSI Technology, Systems, and Applications IEEE, Taipei Section Staff, 2001 Optical Microlithography, 2001 European Conference on Mask Technology for Integrated Circuits and Microcomponents, 2004 In-line Characterization, Yield Reliability, and Failure Analysis in Microelectronics Manufacturing ,2001 **IEEE** International Workshop on Rapid Systems Prototyping IEEE Computer Society. Technical Committee on Simulation, 1999 Contains papers from a June 1999 workshop which brought together system designers model and tool developers integrated circuit designers and software engineers to explore problems and techniques in the area of rapid

system prototyping Papers focus on models for system simulation emulation in a hierarchical sense software to hardware mapping software prototyping and validation prototyping environments of hardware simulators and experiences from specific system prototyping projects Contains sections on communication and distributed systems reconfigurable architectures reuse formal methods design methodologies interface technologies and FPGA based design Lacks a subject index Annotation copyrighted by Book News Inc Portland OR

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