

Figure 8.1 Common-emitter fixed-bias configuration.

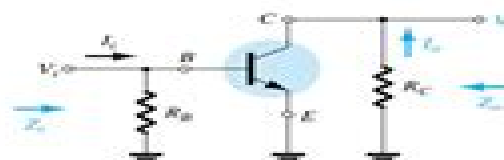
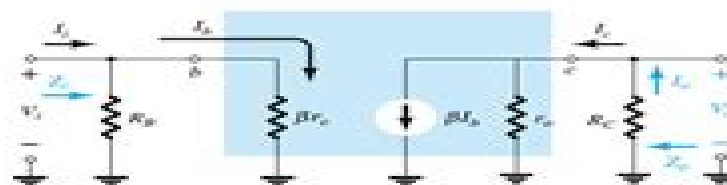


Figure 8.2 Network of Figure 8.1 following the removal of the effects of  $V_{CC}$ ,  $C_1$ , and  $C_2$ .

Figure 8.3 Substituting the  $r_e$  model into the network of Fig. 8.2.



The next step is to determine  $\beta$ ,  $r_e$ , and  $r_c$ . The magnitude of  $\beta$  is typically obtained from a specification sheet or by direct measurement using a curve tracer or transistor testing instrument. The value of  $r_e$  must be determined from a dc analysis of the system, and the magnitude of  $r_c$  is typically obtained from the specification sheet or characteristics. Assuming that  $\beta$ ,  $r_e$ , and  $r_c$  have been determined will result in the following equations for the important two-port characteristics of the system.

$Z_i$  Figure 8.3 clearly reveals that

$$Z_i = R_B \parallel \beta r_e \quad \text{ohms} \quad (8.1)$$

For the majority of situations  $R_B$  is greater than  $\beta r_e$  by more than a factor of 10 (recall from the analysis of parallel elements that the total resistance of two parallel resistors is always less than the smallest and very close to the smallest if one is much larger than the other), permitting the following approximation:

$$Z_i \approx \beta r_e \quad R_B \gg \beta r_e \quad \text{ohms} \quad (8.2)$$

$Z_o$ : Recall that the output impedance of any system is defined as the impedance  $Z_o$  determined when  $V_i = 0$ . For Fig. 8.3, when  $V_i = 0$ ,  $I_i = I_b = 0$ , resulting in an open-circuit equivalence for the current source. The result is the configuration of Fig. 8.4.

$$Z_o = R_C \parallel r_c \quad \text{ohms} \quad (8.3)$$

If  $r_c \approx 10 R_C$ , the approximation  $R_C \parallel r_c \approx R_C$  is frequently applied and

$$Z_o \approx R_C \quad r_c \gg 10 R_C \quad (8.4)$$

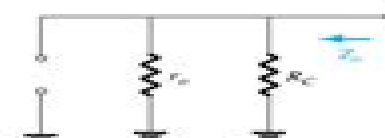


Figure 8.4 Determining  $Z_o$  for the network of Fig. 8.3.

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