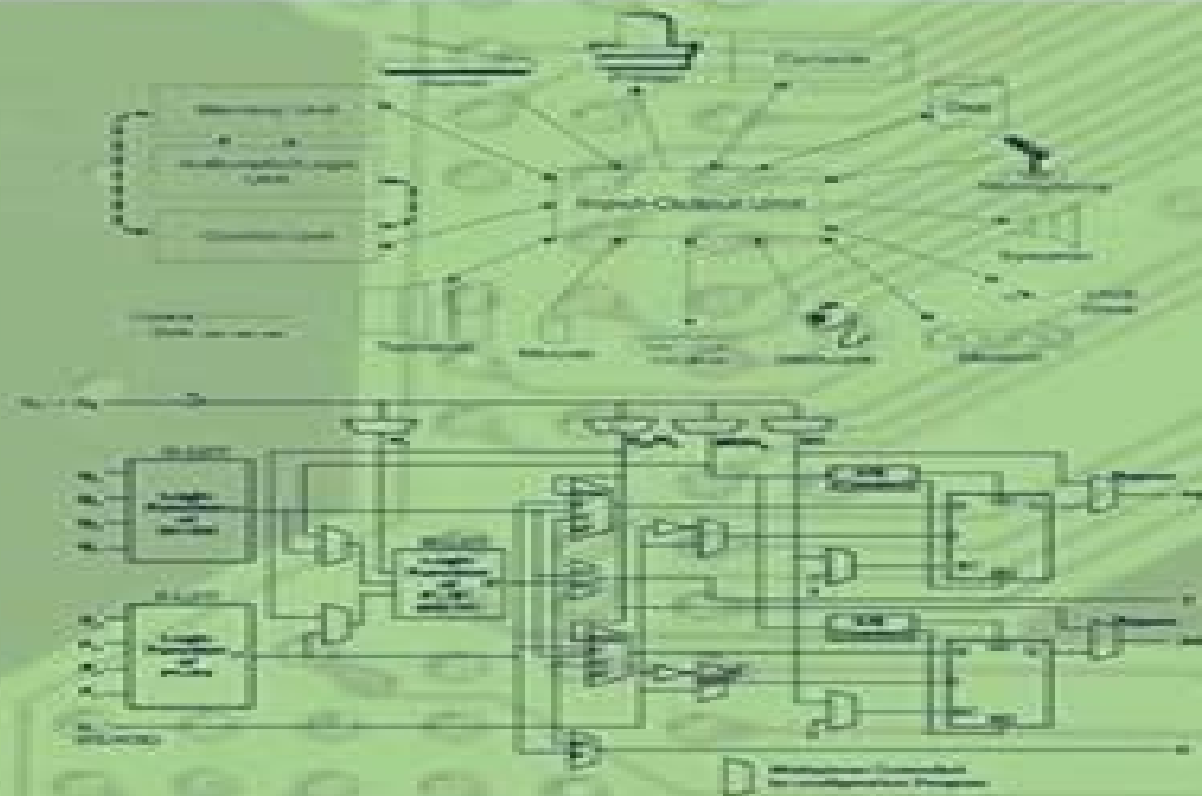




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Computer Organization V. Carl Hamacher,Zvonko G. Vranesic,Safwat G. Zaky,1996 This text is intended to be of use for first courses in computer architecture taught in computer science and electrical computing engineering departments The fourth edition places more emphasis on design and covers topics such as RISC processors performance analysis and memory systems

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02 01 11 12 13 1 t1 t2 t3 motor 23 24 13 14 tr ist 28 run

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