BIII-258 Lê Phương Thu

COMPUTER ARCHITECTURE

- Consider a 32-bit computer with a clock frequency of 500 MHz, and the structure of the computer shown as bellows.
 Control and state registers
- Program counter, PC
- Instruction register, RI
- Memory address register, MAR
- Memory buffer register, MBR
- Program status word, PWS

The memory is byte addressed and requires two cycles for reading and writing operations.

- a) Indicate the elemental operations needed to execute the MIPS32 instructions a-1 lw R1, (R2) a-2 addi R0, R1, 10
- If the average number of clock cycles needed to execute an instruction is 25, calculate the average number of instructions executed by this computer in one second.
- c) If the computer executes 200 MIPS, what is the average number of cycles per instruction?
- d) The following control signals activated in a clock cycle.

C1: T8, C1

C2: R, Td

C3: R, C2

C4: RC= R1, T3, SC

- Indicate the elemental operations corresponding to these control signals.
- What is the machine instruction corresponding to these control signals?

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William B. Foxhall

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- 1. Understanding the eBook Computer Architecture And Organization Exercises Solutions Answer
 - The Rise of Digital Reading Computer Architecture And Organization Exercises Solutions Answer
 - Advantages of eBooks Over Traditional Books
- 2. Identifying Computer Architecture And Organization Exercises Solutions Answer
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
- 3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Computer Architecture And Organization Exercises Solutions Answer
 - User-Friendly Interface
- 4. Exploring eBook Recommendations from Computer Architecture And Organization Exercises Solutions Answer

- Personalized Recommendations
- Computer Architecture And Organization Exercises Solutions Answer User Reviews and Ratings
- Computer Architecture And Organization Exercises Solutions Answer and Bestseller Lists
- 5. Accessing Computer Architecture And Organization Exercises Solutions Answer Free and Paid eBooks
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 - Computer Architecture And Organization Exercises Solutions Answer eBook Subscription Services
 - Computer Architecture And Organization Exercises Solutions Answer Budget-Friendly Options
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 - Computer Architecture And Organization Exercises Solutions Answer Compatibility with Devices
 - Computer Architecture And Organization Exercises Solutions Answer Enhanced eBook Features
- 7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Computer Architecture And Organization Exercises Solutions Answer
 - Highlighting and Note-Taking Computer Architecture And Organization Exercises Solutions Answer
 - Interactive Elements Computer Architecture And Organization Exercises Solutions Answer
- 8. Staying Engaged with Computer Architecture And Organization Exercises Solutions Answer
 - o Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Computer Architecture And Organization Exercises Solutions Answer
- 9. Balancing eBooks and Physical Books Computer Architecture And Organization Exercises Solutions Answer
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Computer Architecture And Organization Exercises Solutions Answer
- 10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
- 11. Cultivating a Reading Routine Computer Architecture And Organization Exercises Solutions Answer
 - Setting Reading Goals Computer Architecture And Organization Exercises Solutions Answer
 - Carving Out Dedicated Reading Time
- 12. Sourcing Reliable Information of Computer Architecture And Organization Exercises Solutions Answer

- Fact-Checking eBook Content of Computer Architecture And Organization Exercises Solutions Answer
- Distinguishing Credible Sources
- 13. Promoting Lifelong Learning
 - Utilizing eBooks for Skill Development
 - Exploring Educational eBooks
- 14. Embracing eBook Trends
 - Integration of Multimedia Elements
 - Interactive and Gamified eBooks

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