

UNIVERSITY GRANTS COMMISSION

VACANCY

The University Grants Commission being the apex body of the University System will entertain applications from suitably qualified candidates for the following post at the Secretariat.

1. POST OF COMPUTER PROGRAMMER

Qualifications :

(i) Bachelors Degree of a recognized University

and

 (ii) Ability to programme computers in one or more languages i.e, Microsoft. NET, PHP, Python, C/C + +

and

Thorough knowledge in SQL using Database Management Systems such as MYSQL and MS SQL.

Salary Scale: U - AS 1 (II) - Rs. 41,095 - 18 x 660 - Rs. 52,975/- p.m.

(Other government approved allowances will be paid)

Recruitment: Selection will be made by an aptitude test and interview

OTHER BENEFITS APPLICABLE TO THE POST:

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(ii) Employees Trust Fund

3% of the salary by the employer.

(iii) Gratuity

Method of

Payment of Gratuity will be in accordance with the provisions of the Payment of Gratuity Act. No.12 of 1983.

APPLICATION PROCEDURE:

- (i) Duly filled specimen application form should be sent to Senior Assistant Secretary/ Personnel, University Grants Commission, No: 20, Ward Place, Colombo 07 under registered cover along with copies of relevant certificates to reach on or before 15th May 2024 (Specimen application forms are available on the UGC website, i.e. www.ugc.ac.lk). Applications sent in the specified form will only be accepted.
- (ii) The name of the post in respect of which the applications are sent, should be stated on the top left hand corner of the envelope.
- (iii) Candidates in the service of the University Grants Commission and Higher Educational Institutions/Institutes, Government Departments, State Corporations and Statutory Boards should channel their applications through the Heads of their respective Institutions. Applications which are not forwarded through the respective Heads of Departments will be rejected.
- (iv). Applications received after the closing date and incomplete applications will not be entertained.
- (v). University Grants Commission reserves the right to shortlist the candidates by following a suitable method.

Secretary University Grants Commission No: 20, Ward Place Colombo-07. 25.04.2024

Computer System Architecture University Grants Commission

Clifford Lane

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VLSI Circuits and Embedded Systems Hafiz Md. Hasan Babu, 2022-07-29 Very Large Scale Integration VLSI creates an integrated circuit IC by combining thousands of transistors into a single chip While designing a circuit reduction of power consumption is a great challenge VLSI designs reduce the size of circuits which eventually reduces the power consumption of the devices However it increases the complexity of the digital system Therefore computer aided design tools are introduced into hardware design processes Unlike the general purpose computer an embedded system is engineered to manage a wide range of processing tasks Single or multiple processing cores manage embedded systems in the form of microcontrollers digital signal processors field programmable gate arrays and application specific integrated circuits Security threats have become a significant issue since most embedded systems lack security even more than personal computers Many embedded systems hacking tools are readily available on the internet Hacking in the PDAs and modems is a pervasive example of embedded systems hacking This book explores the designs of VLSI circuits and embedded systems These two vast topics are divided into four parts In the book s first part the Decision Diagrams DD have been covered DDs have extensively used Computer Aided Design CAD software to synthesize circuits and formal verification The book s second part mainly covers the design architectures of Multiple Valued Logic MVL Circuits MVL circuits offer several potential opportunities to improve present VLSI circuit designs The book s third part deals with Programmable Logic Devices PLD PLDs can be programmed to incorporate a complex logic function within a single IC for VLSI circuits and Embedded Systems The fourth part of the book concentrates on the design architectures of Complex Digital Circuits of Embedded Systems As a whole from this book core researchers academicians and students will get the complete picture of VLSI Circuits and Embedded Systems and their Reversible and DNA Computing Hafiz M. H. Babu, 2020-11-09 Master the subjects of reversible computing applications and DNA computing with this expert volume Reversible and DNA Computing offers readers new ideas and technologies in the rapidly developing field of reversible computing World renowned researcher and author Hafiz Md Hasan Babu shows readers the fundamental concepts and ideas necessary to understand reversible computing including reversible circuits reversible fault tolerant circuits and reversible DNA circuits Reversible and DNA Computing contains a practical approach to understanding energy efficient DNA computing In addition to explaining the foundations of reversible circuits the book covers topics including Advanced logic design An introduction to the fundamentals of reversible computing Advanced reversible logic synthesis Reversible fault tolerance Fundamentals of DNA computing Reversible DNA logic synthesis DNA logic design This book is perfect for undergraduate and graduate students in the physical sciences and engineering as well as those working in the field of quantum computing It belongs on the bookshelves of anyone with even a passing interest in nanotechnology energy efficient computing and DNA computing **Quantum Biocomputing in Quantum Biology** Volume I Hafiz Md. Hasan Babu, 2025-07-01 The book starts with the basics of Quantum Computing Biocomputing Quantum

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